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|-------|------|--|---|------------------|---------|------------------|
| L1 | 85 | (asynchronous same (different adj frequenc\$3) same phase\$2) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 16:55 |
| L2 | 6 | (asynchronous same (different adj frequenc\$3) same (different adj phase\$2)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 16:55 |
| S1 | 1564 | 716/6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 11:14 |
| S2 | 1966 | 716/5 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 11:14 |
| S3 | 2546 | 716/4 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 11:14 |
| S4 | 3 | (716/6).ccls. and ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 same error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 11:26 |
| S5 | 1 | (716/5).ccls. and ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 same error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 11:28 |
| S6 | 2 | (716/4).ccls. and ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 same error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 11:29 |

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| S7 | 3 | ("716"/\$).ccls. and ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 same error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 11:29 |
| S8 | 60 | ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 same error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 11:30 |
| S9 | 28 | ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 same error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) and ((adequate or proper or sufficient) adj synchroni\$8) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 13:48 |
| S10 | 1 | ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 adj error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) and ((adequate or proper or sufficient) adj synchroni\$8) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 12:26 |
| S11 | 0 | ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 adj error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) and ((adequate or proper or sufficient) adj synchroni\$8)).CLM. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 12:26 |
| S12 | 1 | ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (synchron\$8 adj error\$2) and (metastab\$6) and (hold same ((set adj up) or setup) same time) and ((adequate or proper or sufficient) adj synchroni\$8) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 13:49 |
| S13 | 1 | ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (metastab\$6) and (hold same ((set adj up) or setup) same time) and ((synchronizer\$2 or synchroniser\$2) same ((adequate or proper or sufficient) adj synchroni\$8)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 13:54 |

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| S14 | 1 | ((clock dj (environment\$2 or domain\$2)) same (cross\$4)) and (metastab\$6) and ((synchronizer\$2 or synchroniser\$2) same ((adequate or proper or sufficient) adj synchroni\$8)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 13:56 |
| S15 | 3 | (clock dj (environment\$2 or domain\$2)) and (metastab\$6) and ((synchronizer\$2 or synchroniser\$2) same ((adequate or proper or sufficient) adj synchroni\$8)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 14:12 |
| S16 | 1 | (clock dj (environment\$2 or domain\$2)) and (metastab\$6) and ((synchronizer\$2 or synchroniser\$2) same ((adequate or proper or sufficient) adj synchroni\$8)) and (clock\$2 same frequenc\$4 same phase\$2) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 14:15 |
| S17 | 2888 | (asynchronous same frequenc\$3 same phase\$2) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 16:28 |
| S18 | 201 | synchronizer and (asynchronous same frequenc\$3 same phase\$2) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 16:29 |
| S19 | 3 | synchronizer and (simulat\$6 same asynchronous same frequenc\$3 same phase\$2) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 16:36 |
| S20 | 31 | synchronizer and (simulat\$6) and (asynchronous same frequenc\$3 same phase\$2) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/06 16:54 |

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the transmitter has the higher clock **frequency**, equivalent designs can be used with the rate multiplier in the transmitter's **clock domain**. ...

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probabilities of **synchronization** failure using very simple hardware. ... exactly the same **frequency**, only the relative **phase** difference is unknown. This is ...

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EP1031093

thus, although the **synchronization** circuit only knows the relative **phase** ... the basic idea behind the **clock domain crossing** Circuitry is straightforward. ...

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The memory and conversion functions are in a same **clock domain**. ... When the output samples of the SRC 730 are exactly **frequency** and **phase** locked to the ...

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probability of **synchronizer** failure to acceptable levels while not introducing ... Therefore with n **clock domain crossings** a minimum latency of $4 \cdot n$ clock ...

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... 3-17 **Clock Domain Crossing**

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 Pettit, R.H.; Wahlen, B.E.; Mai, C.Y.; North, R.C.;
[Military Communications Conference, 2001. MILCOM 2001. Communications for Centric Operations: Creating the Information Force. IEEE](#)
 Volume 2, 28-31 Oct. 2001 Page(s):1219 - 1223 vol.2
 Digital Object Identifier 10.1109/MILCOM.2001.986044
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- ☐ 2. **High resolution multi-frequency digital phase locked loop**
 Efendovich, A.; Afek, Y.; Sella, C.; Bikowsky, Z.;
[Circuits and Systems, 1993. ISCAS '93, 1993 IEEE International Symposium on](#)
 3-6 May 1993 Page(s):1128 - 1131 vol.2
 Digital Object Identifier 10.1109/ISCAS.1993.393903
[AbstractPlus](#) | Full Text: [PDF](#)(336 KB) IEEE CNF
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- ☐ 3. **Analysis of true jitter arising from pulse-stuffing schemes**
 Abeysekera, S.S.;
[Communications, IEEE Transactions on](#)
 Volume 51, Issue 4, April 2003 Page(s):597 - 606
 Digital Object Identifier 10.1109/TCOMM.2003.810802
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- ☐ 4. **A research on adaptive PCM clock recovering**
 Wu Zhilu; Yin Zhendong; Ren Guanghui; Yang Shuiwang;
[Microwave and Millimeter Wave Technology, 2002. Proceedings. ICMMT 2002 International Conference on](#)
 17-19 Aug. 2002 Page(s):406 - 409
[AbstractPlus](#) | Full Text: [PDF](#)(283 KB) IEEE CNF
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- ☐ 5. **Joint carrier phase and symbol timing synchronization for burst satellite**
 Yigang Fan; Chakravarthi, P.;
[MILCOM 2000. 21st Century Military Communications Conference Proceeding](#)
 Volume 2, 22-25 Oct. 2000 Page(s):1104 - 1108 vol.2


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- ☐ 1. **A fast resolving BiNMOS synchronizer for parallel processor interconnect**
 Jex, J.; Dike, C.;
Solid-State Circuits, IEEE Journal of
 Volume 30, Issue 2, Feb. 1995 Page(s):133 - 139
 Digital Object Identifier 10.1109/4.341740
[AbstractPlus](#) | Full Text: [PDF\(528 KB\)](#) IEEE JNL
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- ☐ 2. **Timing measurements of synchronization circuits**
 Semiat, Y.; Ginosar, R.;
Asynchronous Circuits and Systems, 2003. Proceedings. Ninth International S
 12-15 May 2003 Page(s):68 - 77
 Digital Object Identifier 10.1109/ASYNC.2003.1199167
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- ☐ 3. **Cross-layer sensor network synchronization**
 Tian, Z.; Luo, X.; Giannakis, G.B.;
Signals, Systems and Computers, 2004. Conference Record of the Thirty-Eigh
Conference on
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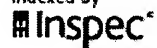
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